

## Bhakta, et al.

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ARRANGEMENT OF INTEGRATED CIRCUITS IN A MEMORY  
MODULE

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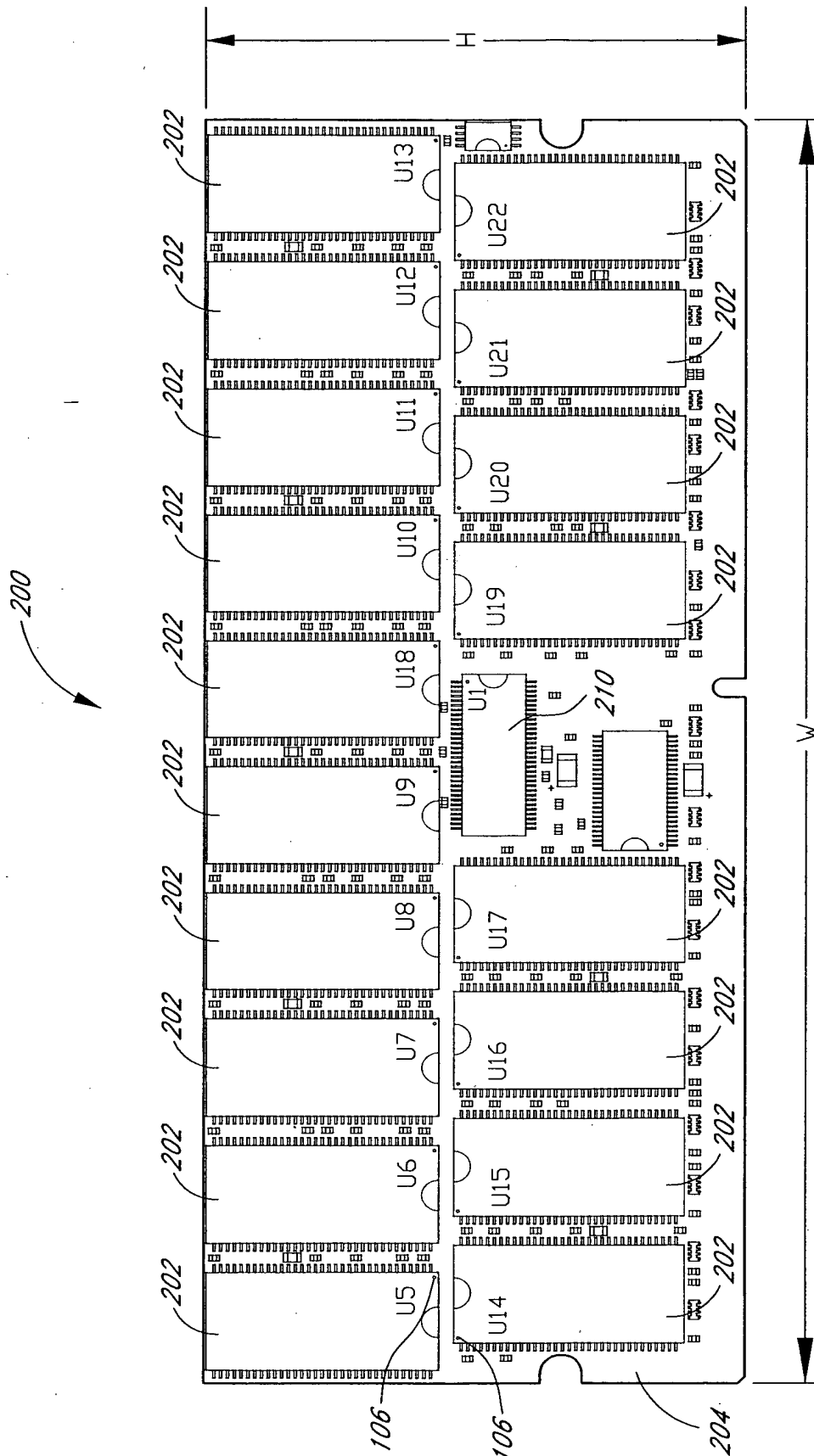


FIG. 2A

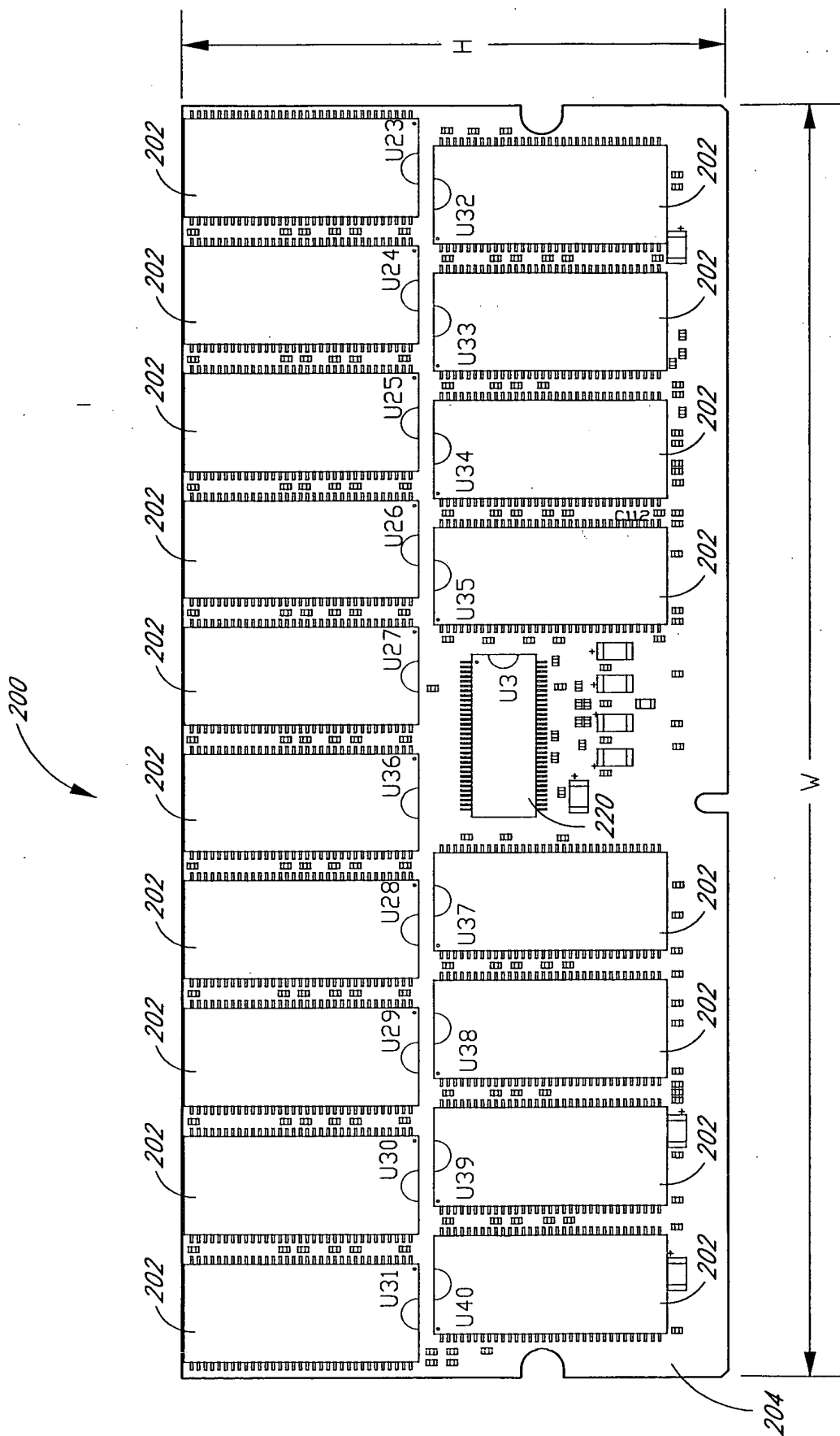
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# ARRANGEMENT OF INTEGRATED CIRCUITS IN A MEMORY MODULE

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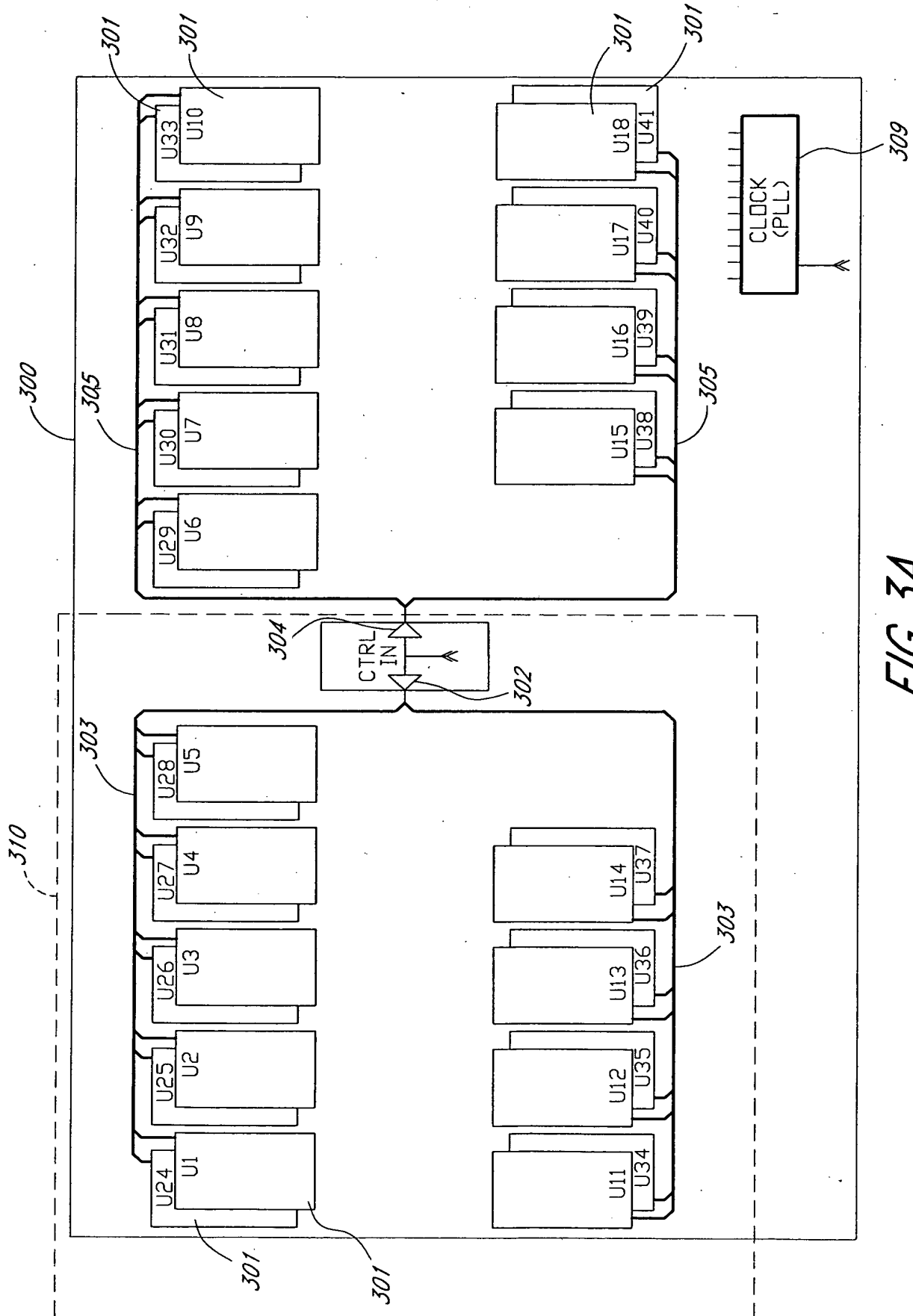


FIG. 3A

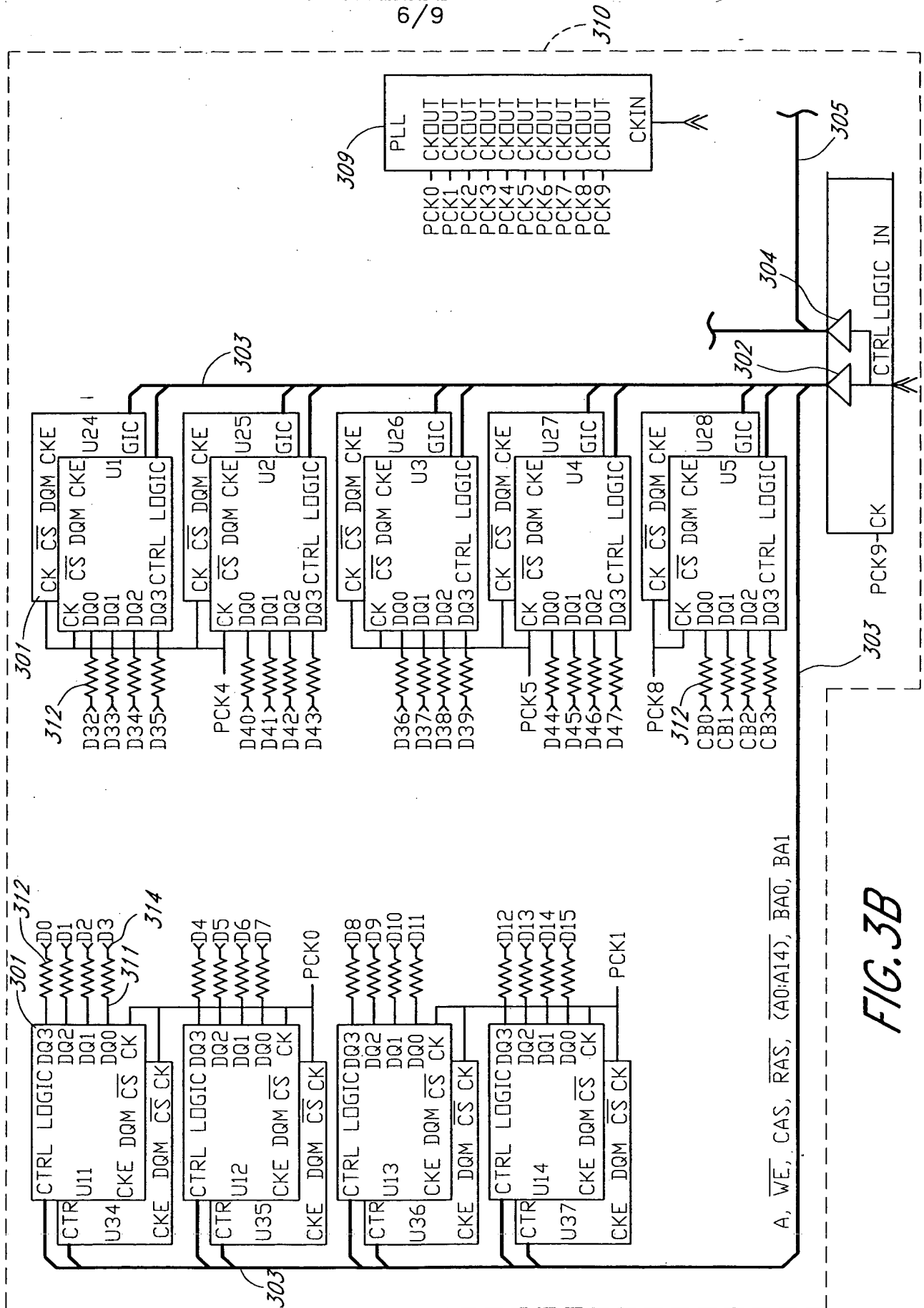
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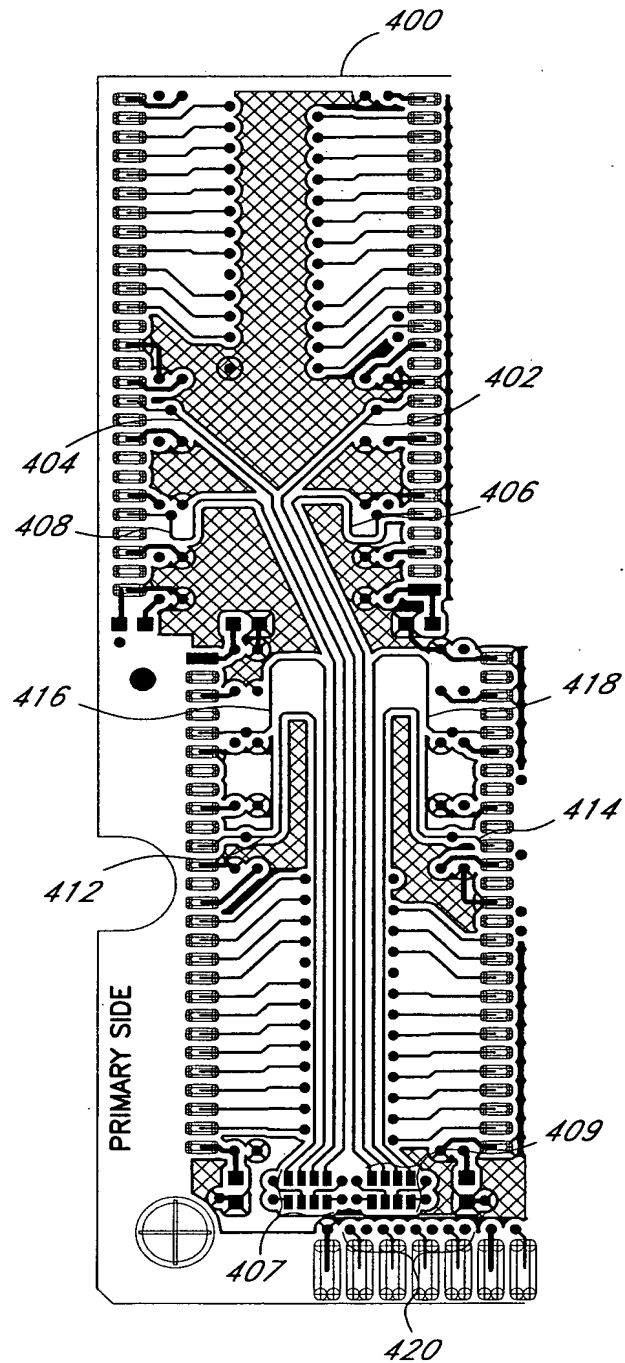


FIG. 4A

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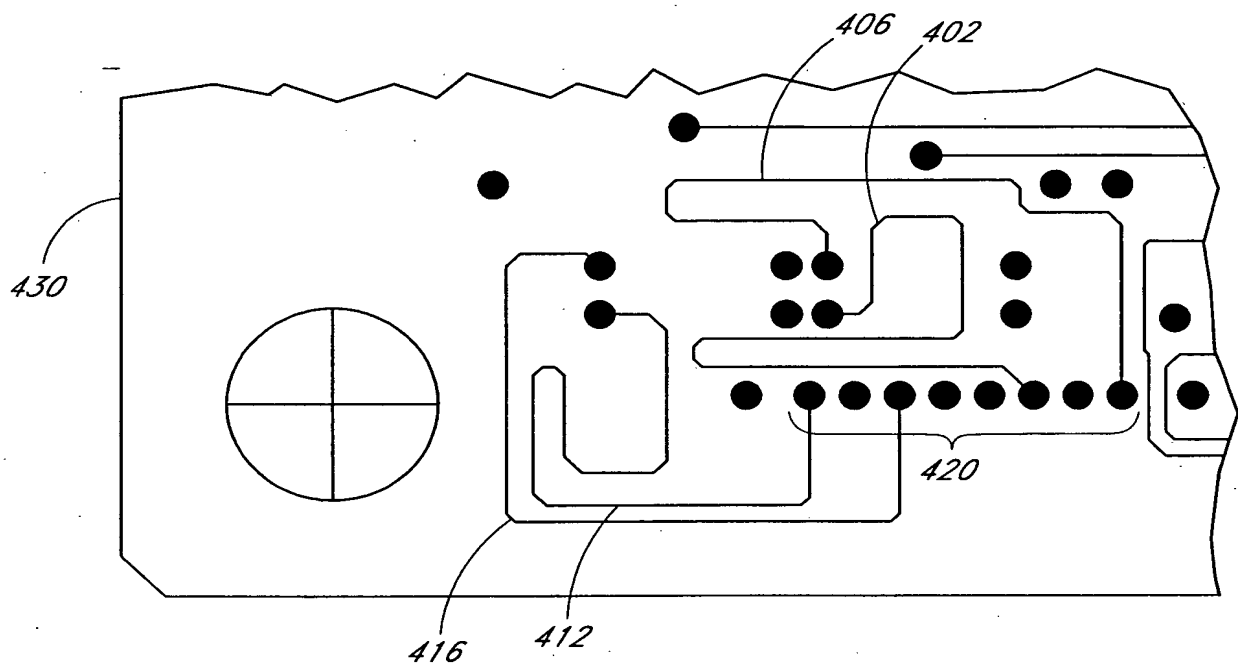


FIG. 4B



ARRANGEMENT OF INTEGRATED CIRCUITS IN A MEMORY  
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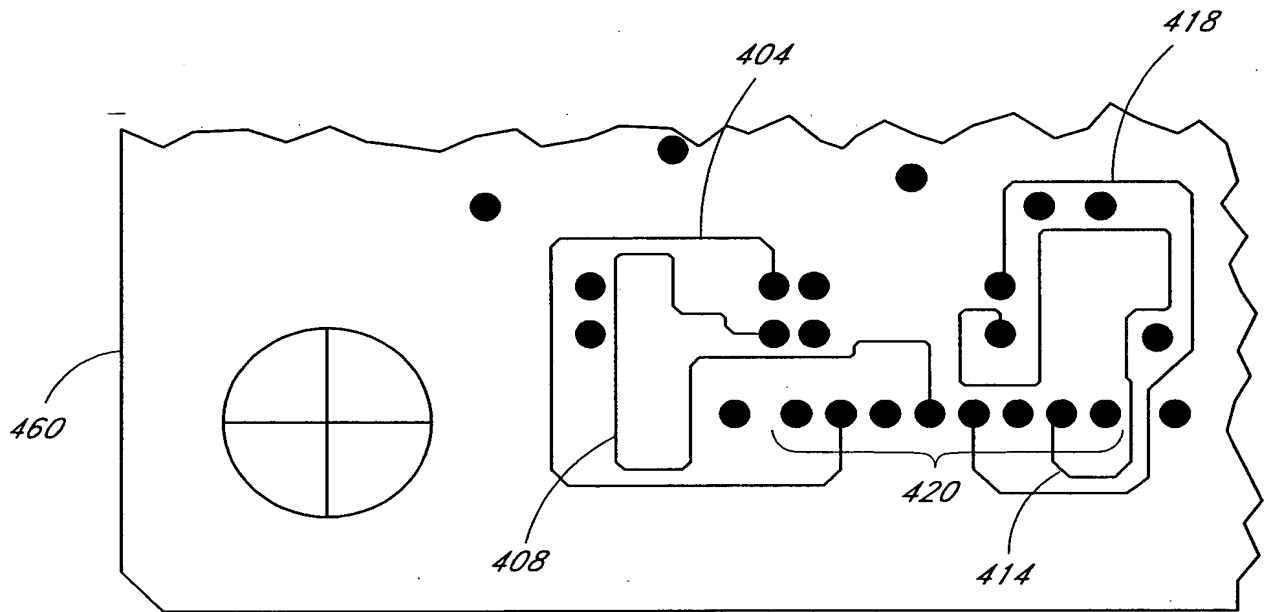


FIG. 4C

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